



**In The United States Patent and Trademark Office**

Application Number: 10/692,683

Examiner: HSU, Joni

Applicants: CHEN, Chia-Hsin

Group Art Unit: 2676

Filing Date: 10/27/2003

Title: Method for an Image Reducing Processing Circuit

Date: July 5, 2005

**AMENDMENT A**

Honorable Commissioner for Patents,  
P.O. Box 1450,  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed 01/26/2005, kindly amend the above application as follows.